

an oscillator connected between the other end of the first MOS transistor and a second power source;

a first capacitive element connected to the oscillator in parallel; and

additive control means that controls the oscillation frequency of the oscillator separately from the first MOS transistor by a digital signal.

2. (amended) A voltage controlled oscillator according to Claim 1, wherein:

a1  
conf the additive control means controls the oscillation frequency of the oscillator by the digital signal which is a 1-bit digital signal.

---

9. (amended) A PLL circuit that generates an output signal having a phase following the phase of a periodic reference signal and having a frequency of predetermined integral times, comprising:

92 a voltage controlled oscillator for generating the output signal;

a frequency divider that divides the output of the voltage controlled oscillator;

a phase comparator that generates a phase detection signal for differentiating a lead or delay of the phase of the

output of the frequency divider to the phase of the reference signal;

a frequency comparator that discriminates the occurrence of difference between the frequency of the reference signal and the frequency of an output signal from the frequency divider and generates discrimination output; and

a control circuit that generates an analog control signal according to the output of at least the frequency comparator, wherein:

the voltage controlled oscillator is provided with a MOS transistor one end of which is connected to a first power source and to the gate electrode of which the analog control signal is input, an oscillator connected between the MOS transistor and a second power source, a capacitive element connected to the oscillator in parallel and additive control means that controls the oscillation frequency by a phase detection signal output from the phase comparator as a digital signal.

92  
Cont

#### REMARKS

The objection to claim 2 and the indication that claim 2 would be allowable if rewritten in independent form including all of the limitations of the base claim and intervening claims, is acknowledged. By the present amendment, a portion of the features of claim 2 have been incorporated into parent